Amendments to the Claims

2	This listing of claims will replace all prior versions, and listings, of claims in the application:
3	Claim 1 (Currently Amended): A light emitting device comprising:
4	a substantially transparent spherical housing;
5	a circuit contained within the housing, the circuit comprising a light emitting means, a
6	time delay means, a battery means, means for ascertaining if the voltage of the battery
7	means is less than 7 volts d.c., and a switch which, upon activation, closes the circuit
	providing current to the circuit from the battery means.
8	Claim 2 (Original): The light emitting device of claim 1 wherein the housing is generally
9	spherical having a flat base.
10	Claim 3 (Original): The light emitting device of claim 2 wherein the housing contains self-
11	righting means for causing the device, following deployment on a surface, to come to a
12	resting position with the flat base engaging the surface.
	Claim 4 (Original): The light emitting device of claim 1 wherein a switch extension is
13	configured to engage the switch, a portion of the switch extension extending to the
14	exterior of the housing.
15	Claim 5 (Original): The light emitting device of claim 4 wherein the exterior of the housing
16	comprises a recessed section, a portion of the switch extension extending into the
17	recessed section.
18	Claim 6 (Original): The light emitting device of claim 1 wherein the light emitting means emits
	light within the infrared spectrum.
19	Claim 7 (Original): The light emitting device of claim 1 wherein the housing comprises a first
20	hemisphere and a second hemisphere attached together with fastening means.
21	Claim 8 (Original): The light emitting device of claim 7 wherein a circuit board is set between
22	the first hemisphere and the second hemisphere.
23	Claim 9 (Original): The light emitting device of claim 8 wherein the light emitting means, the
	time delay means and the switch are mounted on the circuit board.
24	Claim 10 (Original): The light emitting device of claim 1 wherein the time delay means
25	comprises a programmable time delay for energizing of the light emitting means.

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1	Claim 11 (Original): The light emitting device of claim 1 wherein the light emitting means
	comprises a first set of light emitting diodes and a second set of light emitting diodes.
2	Claim 12 (Original): The light emitting device of claim 11 wherein the circuit is configured so
3	that the first set of light emitting diodes and the second set of light emitting diodes are not
4	energized at the same time.
5	Claim 13 (Original): The light emitting device of claim 12 wherein the first set of light emitting
6	diodes and the second set of light emitting diodes are alternatively energized at 150 cycles
	per second.
7	Claim 14 (Original): The light emitting device of claim 1 wherein the battery means comprises a
8	9 volt dc battery.
9	Claim 15 (Canceled).
10	Claim 16 (Currently Amended): The light emitting device of [claim 15] claim 1 wherein the
11	circuit further comprises means for temporarily interrupting current flow to all of the light
12	emitting means if the battery voltage is less than 7 volts dc.
	Claim 17 (Original): The light emitting device of claim 1 wherein the circuit comprises a
13	microcontroller.
14	Claim 18 (Original): The light emitting device of claim 17 wherein the microcontroller is
15	packaged as a TSSOP.
16	Claim 19 (Original): The light emitting device of claim 18 wherein the TSSOP comprises 16
17	leads.
	Claim 20 (Original): The light emitting device of claim 1 wherein the circuit further comprises a
18	voltage regulator.
19	Claim 21 (Currently Amended): A light emitting device comprising:
20	a substantially transparent generally spherical housing, the housing comprising a first
21	hemisphere and a second hemisphere attached together with fastening means;
22	a round circuit board set between the first hemisphere and the second hemisphere wherein
	the circuit board has a rectangular opening in the approximate mid-section of the circuit
23	<u>board;</u>
24	a battery storage compartment contained within the housing;
25	a circuit comprising a light emitting means, a time delay means, a battery, and a switch
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1	which, upon activation, closes the circuit providing current to the circuit from the battery;
	and
2	the light emitting means, the time delay means and the switch mounted on the circuit
3	board and the battery disposed within the battery storage compartment.
4	Claim 22 (Canceled).
5	Claim 23 (Currently Amended): The light emitting device of [claim 22] claim 21 wherein the
6	battery storage compartment extends through the rectangular opening in the circuit board.
7	Claim 24 (Original): The light emitting device of claim 21 wherein a nine volt battery is
	disposed within the battery storage compartment.
8	Claim 25 (Original): The light emitting device of claim 21 wherein a switch extension is
9	configured to engage the switch, a portion of the switch extension extending to the
10	exterior of the housing.
11	Claim 26 (Original): The light emitting device of claim 25 wherein the exterior of the housing
12	comprises a recessed section, a portion of the switch extension extending into the
	recessed section.
13	Claim 27 (Original): The light emitting device of claim 21 wherein the light emitting means
14	comprises a plurality of light emitting diodes.
15	Claim 28 (Original): The light emitting device of claim 27 wherein the light emitting diodes
16	emit light within the infrared spectrum.
17	Claim 29 (Original): The light emitting device of claim 21 where the time delay means delays
	energizing of the light emitting means for five seconds.
18	Claim 30 (Original): The light emitting device of claim 27 wherein the plurality of light
19	emitting diodes comprises a first set of light emitting diodes and a second set of light
20	emitting diodes.
21	Claim 31 (Original): The light emitting device of claim 30 wherein the circuit is configured so
22	that the first set of light emitting diodes and the second set of light emitting diodes are not
23	energized at the same time.
	Claim 32 (Original): The light emitting device of claim 31 wherein the first set of light emitting
24	diodes and the second set of light emitting diodes are alternatively energized at 150 cycles
25	per second.
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1	Claim 33 (Currently Amended): A method of strategically illuminating a darkened area
	comprising the steps of:
2	activating a light emitting device having time delay means by engaging a switch on the
3	light emitting device;
4	deploying the light emitting device by propelling the light emitting device into the
5	darkened area; and
6	waiting for a predetermined time to elapse for the light emitting means to emit light,
	wherein the light emitting device comprises: (i) a substantially transparent housing and
7	(ii) a circuit contained within the housing, the circuit comprising light emitting means, the
8	time delay means, battery means, means for ascertaining whether the battery means
9	voltage is less than 7 volts dc, and the switch which, upon activation, closes the circuit.
10	Claim 34 (Original): The method of claim 33 wherein the housing is generally spherical.
11	Claim 35 (Original): The method of claim 33 wherein the housing is generally spherical having a
	flat base.
12	Claim 36 (Original): The method of claim 35 wherein the housing contains self-righting means
13	for causing the device, following deployment on a surface, to come to a resting position
14	with the flat base engaging the surface.
15	Claim 37 (Original): The method of claim 33 wherein a switch extension is configured to engage
16	the switch, a portion of the switch extension extending to the exterior of the housing.
	Claim 38 (Original): The method of claim 37 wherein the exterior of the housing comprises a
17	recessed section, a portion of the switch extension extending into the recessed section.
18	Claim 39 (Original): The method of claim 33 wherein the light emitting means comprises a
19	plurality of light emitting diodes.
20	Claim 40 (Original): The method of claim 39 wherein the light emitting diodes emit light within
21	the infrared spectrum.
	Claim 41 (Original): The method of claim 33 wherein the housing comprises a first hemisphere
22	and a second hemisphere attached together with fastening means.
23	Claim 42 (Original): The method of claim 41 wherein a circuit board is set between the first
24	hemisphere and the second hemisphere.
25	Claim 43 (Original): The method of claim 43 wherein the light emitting means, the time delay
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1	means and the switch are mounted on the circuit board.
1	Claim 44 (Original): The method of claim 33 wherein the time delay means delays energizing of
2	the light emitting means for five seconds.
3	Claim 45 (Original): The method of claim 39 wherein the plurality of light emitting diodes
4	comprises a first set of light emitting diodes and a second set of light emitting diodes.
5	Claim 46 (Original): The method of claim 45 wherein the circuit is configured so that the first
6	set of light emitting diodes and the second set of light emitting diodes are not energized at
	the same time.
7	Claim 47 (Original): The method of claim 46 wherein the first set of light emitting diodes and
8	the second set of light emitting diodes are alternatively energized at 150 cycles per
9	second.
10	Claim 48 (Original): The method of claim 33 wherein the battery means comprises a 9 volt dc
11	battery.
	Claim 49 (Canceled).
12	Claim 50. (Currently Amended): The light emitting device of [claim 49] claim 48 wherein the
13	circuit further comprises means for temporarily interrupting current flow to all of the light
14	emitting means if the battery voltage is less than 7 volts dc.
15	Claim 51 (Original): The method of claim 33 wherein the circuit comprises a microcontroller.
16	Claim 52 (Original): The method of claim 51 wherein the microcontroller is packaged as a
	TSSOP.
17	Claim 53 (Original): The method of claim 52 wherein the TSSOP comprises 16 leads.
18	Claim 54 (Original): The method of claim 33 wherein the circuit further comprises a voltage
19	regulator.
20	Claim 55 (Currently Amended): A method of strategically illuminating a darkened area
21	comprising the steps of:
22	activating a light emitting device having a time delay means by engaging a switch on the
	light emitting device;
23	deploying the light emitting device by propelling the light emitting device into the
24	darkened area; and
25	waiting for a predetermined time to elapse for the light emitting device to emit light,
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1	wherein the light emitting device comprises: (i) a substantially transparent generally
1	spherical housing, the housing comprising a first hemisphere and a second hemisphere
2	attached together with fastening means; (ii) a circuit contained within the housing, the
3	circuit comprising light emitting means, the time delay means, battery means and the
4	switch which, upon activation, closes the circuit; and (iii) a circuit board disposed
5	between the first hemisphere and the second hemisphere wherein the circuit board has a
6	rectangular opening in the approximate mid-section of the circuit board, the light emitting
	means, the time delay means and the switch mounted on the circuit board.
7	Claim 56 (Canceled).
8	Claim 57 (Currently Amended): The method of [claim 56] claim 55 wherein [the] a battery
9	storage compartment extends through the rectangular opening in the circuit board.
10	Claim 58 (Currently Amended): The method of [claim 55] <u>claim 57</u> wherein a nine volt battery
11	is disposed within the battery storage compartment.
	Claim 59 (Original): The method of claim 55 wherein a switch extension is configured to engage
12	the switch, a portion of the switch extension extending to the exterior of the housing.
13	Claim 60 (Original): The method of claim 59 wherein the exterior of the housing comprises a
14	recessed section, a portion of the switch extension extending into the recessed section.
15	Claim 61 (Original): The method of claim 55 wherein the light emitting means comprises a
16	plurality of light emitting diodes.
17	Claim 62 (Original): The method of claim 55 wherein the light emitting means emits light
	within the infrared spectrum.
18	Claim 63 (Original): The method of claim 55 wherein the time delay means delays energizing of
19	the light emitting means for five seconds.
20	Claim 64 (Original): The method of claim 61 wherein the plurality of light emitting diodes
21	comprises a first set of light emitting diodes and a second set of light emitting diodes.
22	Claim 65 (Original): The method of claim 64 wherein the circuit is configured so that the first
	set of light emitting diodes and the second set of light emitting diodes are not energized at the
23	same time.
24	Claim 66 (Original): The method of claim 65 wherein the first set of light emitting diodes and
25	the second set of light emitting diodes are alternatively energized at 150 cycles per second.
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